DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1.   A request for continued examination under 37 CFR 1.114, including the fee set

forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this

application is eligible for continued examination under 37 CFR 1.114, and the fee set

forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action

has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on January

16, 2008 has been entered.

Specification

2.   The amendment filed January 16, 2008 is objected to under 35 U.S.C. 132(a)

because it introduces new matter into the disclosure. 35 U.S.C. 132(a) states that no

amendment shall introduce new matter into the disclosure of the invention. The added

material which is not supported by the original disclosure is as follows:

There is no evidence that the original disclosure supports the data output latch

415 determining **when** to output the nonreal-time data via the data output bus according

to the control of the control unit 411 (see amendment to [0029], lines 7-12).

Applicant is required to cancel the new matter in the reply to this Office Action.

Claim Objections

3.   Claims 6 and 8 are objected to because of the following informalities:

"a control logic unit" in line 3 of claim 6 should be replaced with --a memory

control logic unit-- to differentiate itself from the control logic unit in line 16 of claim 1;

"the control logic unit" in line 5 of claim 6 should be replaced accordingly to --the

memory control logic unit--; "the control logic unit" in line 11 of claim 6 should be

replaced accordingly to --the memory control logic unit--

"an external control signal' in lines 3-4 of claim 6 should be replaced with --

another external control signal-- to differentiate itself from the external control signal in

line 17 of claim 1

"a second internal bus" in line 9 and "the second internal bus" in line 15 are

objected to because there is no prior recitation of "first internal bus" (note that "first

internal bus" is recited in claim 2, and claim 6 does not depend on claim 2)

"to convert a type of the real-time data from TTL to differential or from differential

to TTL" in lines 4-5 of claim 8 should be replaced with --to convert the real-time data

from TTL to differential, or from differential to TTL-- for clarity.

Claim Rejections - 35 USC § 112

4.   The following is a quotation of the first paragraph of 35 U.S.C. 112:

> The specification shall contain a written description of the invention, and of the manner and process of
> making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the
> art to which it pertains, or with which it is most nearly connected, to make and use the same and shall
> set forth the best mode contemplated by the inventor of carrying out his invention.

5.   The following is a quotation of the second paragraph of 35 U.S.C. 112:

> The specification shall conclude with one or more claims particularly pointing out and distinctly
> claiming the subject matter which the applicant regards as his invention.

6.   Claims 1-4, 6-9 are rejected under 35 U.S.C. 112, first paragraph, as failing to

comply with the written description requirement. The claim(s) contains subject matter

which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Claim 1 recites "the control logic unit controls the data output latch to latch the nonreal-time data, and determines **when** to output the nonreal-time data from the data output latch" in lines 22-24. There is no evidence that the original disclosure supports such limitation. Applicant is required to specifically point out the support for such limitation from the original disclosure – by page, paragraph, line numbers, drawings and/or labels to overcome the rejection.

Note that in the Remarks, applicant indicates that "it is obvious for the artisan that once the data output latch 415 extracts the nonreal-time data from the bi-directional bus 41, and the control logic unit 411 controls the data output latch 415 **when** to output the nonreal-time data via the data output bus…because the latched nonreal-time data must be output in accordance with control of the control logic unit 411" (see page 9, lines 9-14). While the latched nonreal-time data must be output in accordance with control of the control logic unit 411, it is **not obvious** from the original disclosure that the control logic unit 411 would control <u>when the data output latch 415 outputs the nonreal-time</u> <u>data</u> via the data output bus, and it is **not obvious** from the original disclosure that the control logic unit 411 determines <u>when to output the nonreal-time data from the data</u> <u>output latch</u>.

7.   Claim 9 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant

regards as the invention. Claim 9 recites the limitation "the microcode instruction" in lines 11-12. There is insufficient antecedent basis for this limitation in the claim.

Allowable Subject Matter

8.   Claims 1-4, 6-9 would be allowable if the 112 rejections and the objections can be overcome.

Response to Arguments

9.   Applicant's arguments with respect to the pending claims have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to TANH Q. NGUYEN whose telephone number is (571)272-4154. The examiner can normally be reached on M-F (9:30AM-6:00PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TARIQ HAFIZ can be reached on (571)272-6729. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/TANH Q. NGUYEN/
Primary Examiner, Art Unit 2182

TQN: June 22, 2008